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PATENT NUMBER and  
ISSUE DATE

U.S. UTILITY Patent Application

APPL NUM	FILING DATE	CLASS	SUBCLASS	GAU	EXAMINER
1005300	01/17/2002	257	17	2811	H. PHAM

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McKee Jeffrey;

\*\*CONTINUING DATA VERIFIED:

\*\* FOREIGN APPLICATIONS VERIFIED:

PG-PUB	DO NOT PUBLISH <input type="checkbox"/>	RESCIND <input type="checkbox"/>
Foreign priority claimed	<input type="checkbox"/> yes <input type="checkbox"/> no	ATTORNEY DOCKET NO
35 USC 119 conditions met	<input type="checkbox"/> yes <input type="checkbox"/> no	303.780US1
Verified and Acknowledged Examiner's initials		
TITLE : Transistor structure having reduced transistor leakage attributes ..		
U.S.DEP'T. OF COMM./PAT & TM-PTO-436L(Rev. 12-94)		

NOTICE OF ALLOWANCE MAILED		CLAIMS ALLOWED	
		Assistant Examiner	
ISSUE FEE		Primary Examiner	
Amount Due	Date Paid	Total Claims	Print Claim for O.G
		DRAWING	
		Sheets Drwg.	Figs.Drwg.
		Print Fig.	
		Application Examiner	
<input type="checkbox"/> TERMINAL DISCLAIMER			
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